TIBTRONIX TECHNOLOGY CO., LTD.

TSPLEG10DI

25Gb/s 10km SFP28 Transceiver Hot Pluggable, Duplex LC, +3.3V, 1310nm,Single mode

2020/6/1





Features:

- > Operating data rate up to 25.78Gbps
- Rate Adaptation
- ➤ Up to 10km transmission distance
- ➤ High sensitivity Pin photodiode and TIA
- ➤ LC duplex connector
- ➤ Hot pluggable 20pin connector
- ➤ Low power consumption <1.2 W
- \triangleright Single +3.3V±5% power supply
- ➤ Compliant with SFF-8472 &IEEE 802.3cc
- > Fully RoHS Compliant
- Operating temperature range:

Commercial: 0°C to +70°C

Industrial: -40°C to +85°C

Application

- Data Center Backbone
- > Ethernet Switches
- ➤ High-speed Servers
- ➤ High-performance Computing Clusters
- SAN, Routers, Hubs, Load Balancer

DESCRIPTION



The TSPLEG10D Transceiver is intended for 10km reach service 25.78Gb/s 1310nm single mode high-speed communications equipment where low-cost, extraordinary performance and reliability are essential. It consumes low power, operates base on 3.3V DC power supply and is offered in the industrial temperature range. They are compliant with SFP28 MSA, SFF-8431 and SFF-8432.

The low jitter and low bit error rate optical assembly features a DML laser transmitter and PIN/TIA receiver. It utilizes internal clock and data recovery (CDR) units on transmitter and the receiver chains for low jitter compliance. The differential AC coupled Tx and Rx data interfaces are CML compatible. The device is Class I laser safety compliant.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions	Min.	Max.	Unit
Storage Temperature	$T_{Storage}$		-40	+85	°C
Relative Humidity	RH		0	+85	%

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
	_	TSPLEG10D 0		70		
Case Temperature	T _C	TSPLEG10DI	-40		85	°C
Power Supply Voltage	Vcc		3.15	3.30	3.45	V
Bit Rate	BR			25.78125		Gbps
Bit Error Ratio	BER				5*10 ⁻⁵	
Max Supported Link Length	L				10	km

ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Power Consumption					1.2	W
Supply Current	Icc				350	mA

TRANSMITTER CHARACTERISTICS

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Center Wavelength	λ		1260		1355	nm
Side-mode Suppression Ratio	SMSR		30			dB
Average Optical Power	P_{avg}		-7		2.0	dBm
Optical Modulation Amplitude	TxOMA		-4			dBm
Transmitter and Dispersion Penalty	TDP				1	dB
Average Launch Power of OFF Transmitter	P _{off}				-30	dBm
Extinction Ratio	ER		3.5			dB
Optical Return Loss Tolerance					20	dB
Transmitter Reflectance					-26	dB



RECEIVER CHARACTERISTICS

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Center Wavelength	λ		1260		1355	nm
Damage Threshold			3			dBm
Receive Power Overload					2	dBm
Receiver Reflectance					-26	dB
Receiver Sensitivity	S	Note1			-12	dBm
LOS Assert	LOSA		-30			dBm
LOS De-Assert	LOS _D				-17	dBm
LOS Hysteresis			0.5			dB

Note1: Measured at 25.78125Gb/s, ER>3.5dBm, PRBS 2³¹-1 and BER better than or equal to 5E-5;

Electric Ports Definition

Parameter	Symbol	Unit	Min	Тур	Max	Note			
Transmitter									
Input Differential Impedance	R _{IN}	Ω		100					
Single-ended Data Input Swing	V _{IN}	mVp-p	90		450				
Transmit Disable Voltage	V _{DIS}	V	2		V _{CCHOST}				
Transmit Enable Voltage	V _{EN}	V	V _{EE}		V _{EE} +0.8				
Transmit Fault Assert Voltage	V _{FA}	V	2		V _{CCHOST}				
Transmit Fault De-Assert Voltage	V _{FDA}	V	V _{EE}		V _{EE} +0.4				
	Receive	r							
Single-ended Data Output Swing	V _{OD}	mVp-p	200		450				
LOS Fault	V _{LOSFT}	V	2		V _{CCHOST}				
LOS Normal	V _{LOSNR}	V	V _{EE}		V _{EE} +0.4				

PIN Assignment

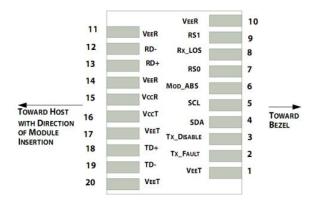


Figure 1.Pin function definitions



TSPLEG10DI

Table 1: Transceiver pin descriptions

Pin Number	Symbol	Name	Description
1,17,20	VeeT	Transmitter Signal Ground	Connected to signal ground on the host board.
2	TX Fault	Transmitter Fault Out (OC)	Module transmitter fault output.
3	TX Disable	Transmitter Disable In (LVTTL)	Module transmitter disable control.
4	SDA	Module Definition	Serial ID with SFF 8472 Diagnostics
5	SCL	Identifiers	Module Definition pins should be pulled up to Host Vcc with $10 \text{ k}\Omega$ resistors.
6	MOD-ABS		
7	RS0	Receiver Rate Select (LVTTL) Transmitter Rate Select (LVTTL)	Rate select 0(Rx):Low=CDR Bypass ; High=CDR Select Rate select 1(Tx):Low=CDR Bypass ;
9	RS1		High=CDR Select
8	LOS	Loss of Signal Out (OC)	Receiver loss of signal.
10,11,14	VeeR	Receiver Signal Ground	Connected to signal ground on the host board.
12	RD-	Receiver Negative DATA Out (CML)	Receiver inverted data output, internally AC coupled and terminated
13	RD+	Receiver Positive DATA Out (CML)	Receiver non-inverted data output, internally AC coupled and terminated.
15	VccR	Receiver Power Supply	Receiver Power 3.3V Supply.
16	VccT	Transmitter Power Supply	Transmitter Power 3.3V Supply.
18	TD+	Transmitter Positive DATA In (CML)	Transmitter non-inverted data input, internally AC coupled and terminated.
19	TD-	Transmitter Negative DATA In (CML)	Transmitter inverted data Input, internally AC coupled and terminated.



Recommended Interface Circuit

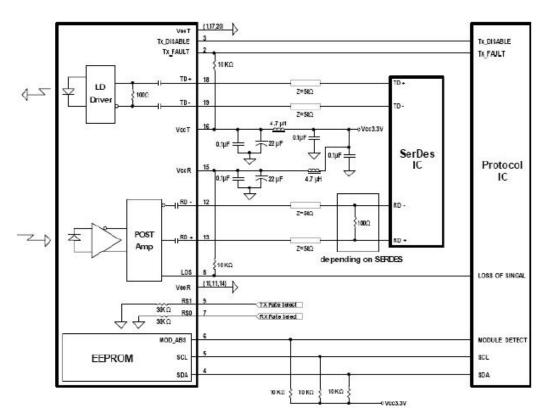


Figure 2. Typical application circuit



Mechanical Dimensions

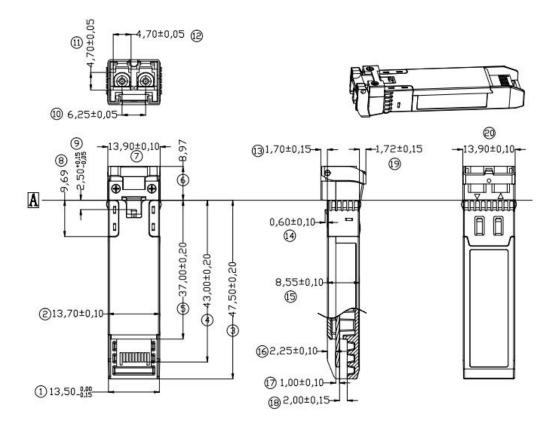


Figure 3. Module Mechanical Dimensions

Digital Diagnostics Functions

As defined by the SFF-8472, Our SFP28 transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- Transceiver temperature
- Laser bias current
- Transmitted optical power
- Received optical power
- Transceiver supply voltage

It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal



range. The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through the 2-wire serial interface. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially. The 2-wire serial interface provides sequential or random access to the 8 bit parameters, addressed from 0x00h to the maximum address of the memory. For more detailed information, including memory map definitions, please refer the SFF-8472 documentation.

Digital Diagnostic Monitor Accuracy

The following characteristics are defined over recommended operating conditions

Parameter	Accuracy	Unit
Internally measured transceiver temperature	+/-3	deg.C
Internally measured transceiver supply voltage	+/-3	%
Measured Tx bias current	+/-10	%
Measured Tx output power	+/-3	dB
Measured Rx received average optical power	+/-3	dB

